



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q65962

Katsuji KIMURA

Appln. No.: 09/940,472

Group Art Unit: 2816

Confirmation No.: 4891

Examiner: Minh T. NGUYEN

Filed: August 29, 2001

For: LINEAR VOLTAGE SUBTRACTOR/ADDER CIRCUIT AND MOS DIFFERENTIAL AMPLIFIER CIRCUIT THEREFOR

APPROVED BY THE EX 9/18/02

SUBMISSION OF DRAWINGS

Commissioner for Patents
Washington, D.C. 20231

Sir:

Submitted herewith please find 3 sheets of drawings (Figures 17-19) in compliance with 37 C.F.R. § 1.84. The Examiner is respectfully requested to acknowledge receipt of these drawings.

The submitted drawings incorporate the proposed drawing changes approved in Paper No. 6.

Respectfully submitted,

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Date: July 31, 2002

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H/T/Braining
Changes

8/8/02

Adm th